

+

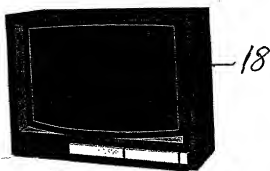
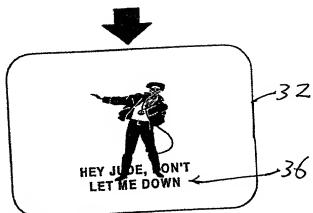
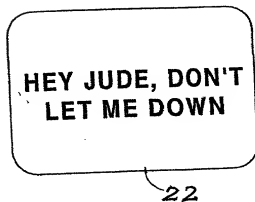


FIG. 1

09736906.1.21300

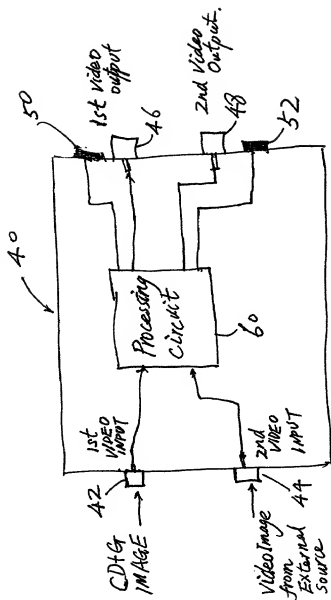


FIG. 2

[illegible]

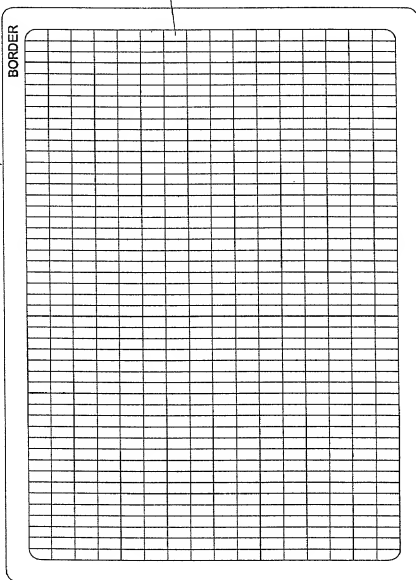
FIGURE 3

70

CDG SCREEN FORMAT

PIXEL

CELL FORMAT



1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16

CELL LINE 1

CELL LINE 12

CELL COLUMN 6

CELL COLUMN 1

72

48

1

FIG. 4

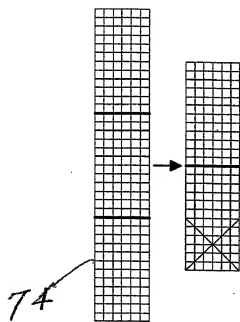


FIG. 5

CELL START POSITION TRANSLATION	
INPUT CELL LINE	OUTPUT CELL LINE START
1	9
2	9.5
3	10
4	10.5
5	11
6	11.5
7	12
8	12.5
9	13
10	13.5
11	14
12	14.5
13	15
14	15.5
15	16
16	16.5

76

FIG. 6

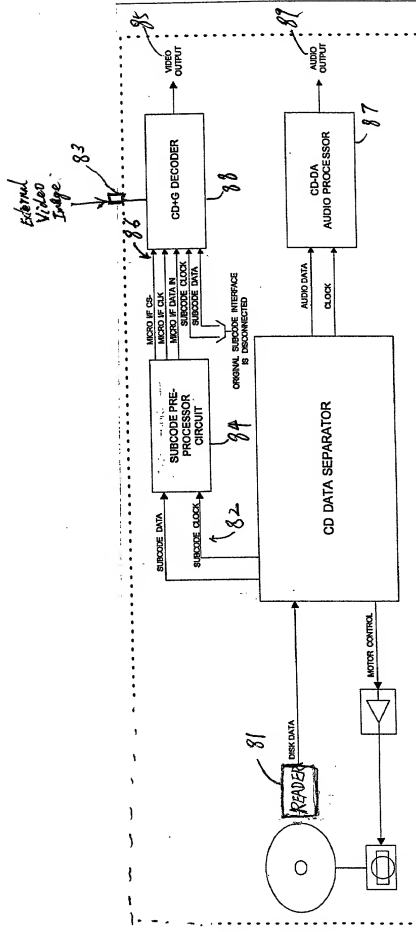


FIG. 7

SUBCODE PRE-PROCESSOR CIRCUIT 84

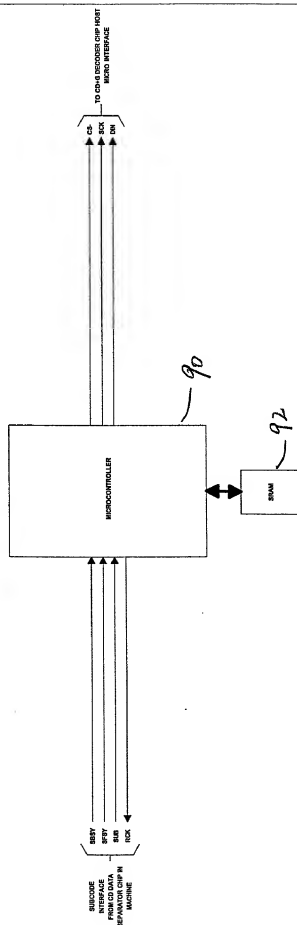


FIG. 8

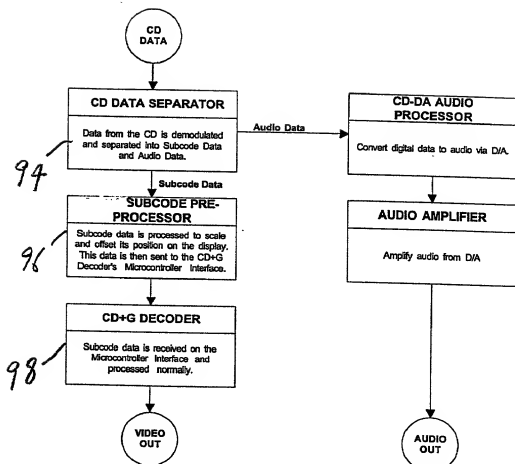


FIG. 9

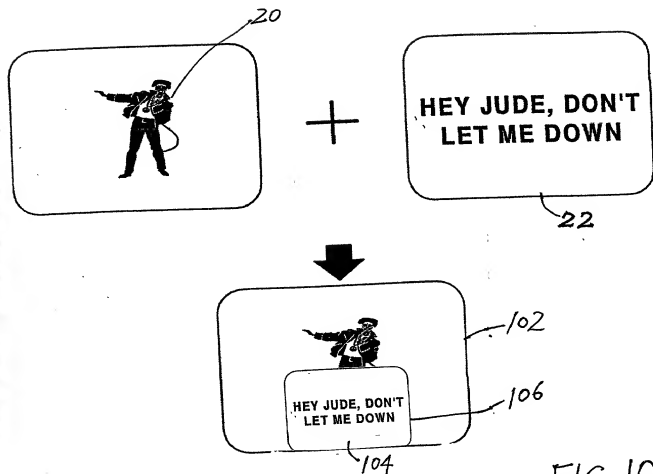


FIG. 10